

Electronic Patent Application Fee Transmittal

Application Number:	10729831			
Filing Date:	05-Dec-2003			
Title of Invention:	NAND memory array incorporating capacitance boosting of channel regions in unselected memory cells and method for operation of same			
First Named Inventor:	En-Hsing Chen			
Filer:	Andrew C. Graham			
Attorney Docket Number:	023-0028			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
Total in USD (\$)				180